

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: **Gary P. Morrison, et al.**

Serial No.: **10/034,827**

Filed: **01/03/2002**

For: **Chip-Scale Packages Stacked on Folded**

Interconnector For Vertical Assembly

On Substrates

Docket: **TI-31373**

Art Unit: **2827**

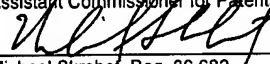
Examiner: **Mitchell, James**

Conf. No.: **4496**

EXTENSION OF TIME

Assistant Commissioner For Patents
Washington, DC 20231

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)	
I hereby certify that on this date the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231.	
	<u>10-16-03</u>
Michael Skrehot, Reg. 36,682	Date

Pursuant to 37 CFR 1.136(a), Applicants respectfully petitions the Commissioner for an extension of the shortened statutory period for response in the above-identified Application.

The fee for this extension is indicated below:

<input checked="" type="checkbox"/> One Month	(\$110)	<input type="checkbox"/> Four Months	(\$1,480)
<input type="checkbox"/> Two Months	(\$420)	<input type="checkbox"/> Five Months	(\$2,010)
<input type="checkbox"/> Three Months	(\$950)		

Any further necessary extension of time is hereby requested. Charge any and all fees to the deposit account of Texas Instruments Incorporated, Account No. 20-0668. An original and two copies of this sheet are enclosed.

10/23/2003 AWONDAF1 00000049 200668 10034827
02 FC:1251 110.00 DA

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Respectfully submitted,



Michael K. Skrehot
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